

800G OSFP to 2x400G OSFP-RHS AEC



0.5m~7m

Black Mesh

Key Features

- Support 8x106.25G PAM4
- InfiniBand NDR & Ethernet compatible
- Maximum link length up to 7m
- Single Wire Size: 26AWG~32AWG
- Maximum power consumption: 12W(800G end), 6.5W (400G end)
- Case temperature range: 0°C to 70°C
- 3.3V Power Supply
- Hot Pluggable
- RoHS Compliance

Applications

- Data center & Networking Equipment
- Servers/Storage Devices
- High Performance Computing (HPC)
- Switches/Routers

Supported Standards

- OSFP MSA
- CMIS 4.0
- IEEE 802.3ck



Ordering Information

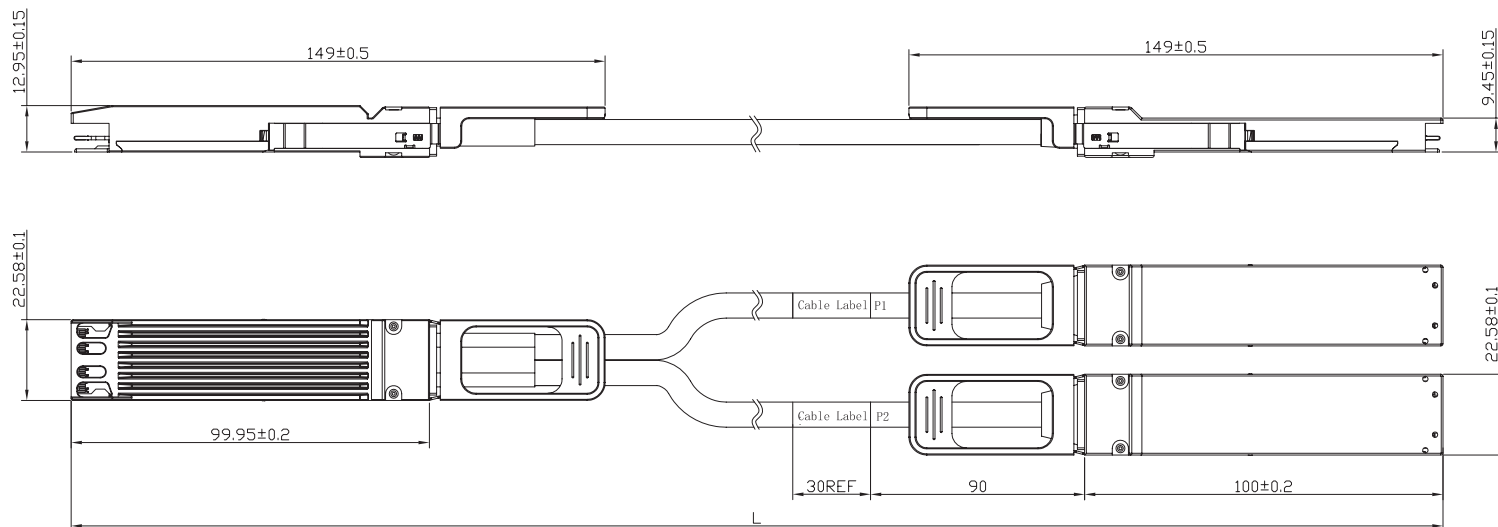
LG00-QM2xxxC

800G OSFP Finned to 2x400G OSFP-RHS AEC – 0.5m~7m

Ordering P/Ns	Description	Length	AWG	Cable	NOTE
LG00-QM2030C	800G OSFP to 2X400G OSFP RHS AEC	3.0m	32AWG	Black Mesh	
LG00-QM2050C	800G OSFP to 2X400G OSFP RHS AEC	5.0m	30AWG	Black Mesh	
LG00-QM2065C	800G OSFP to 2X400G OSFP RHS AEC	6.5m	28AWG	Black Mesh	
LG00-QM2070C	800G OSFP to 2X400G OSFP RHS AEC	7.0m	26AWG	Black Mesh	

xxx define AEC cable length

Mechanical Outline



Absolute Maximum Ratings

Parameter	Symbol	Unit	Min.	Typ.	Max.	Notes
Supply Voltage	Vcc	V	-0.4	-	3.6	
Storage Temperature	TS	°C	-40	-	85	
Operating Case temperature	TOP	°C	0	-	70	
Operating Humidity	RH	%	5	-	85	

Recommended Operating Conditions

Parameter	Unit	Min.	Typ.	Max.	Notes
Supply Voltage	V	3.135	3.3	3.465	
Operating Case Temperature	°C	0		70	
Bit Rate	Gb/s		800		

OSFP PIN Function Definitions

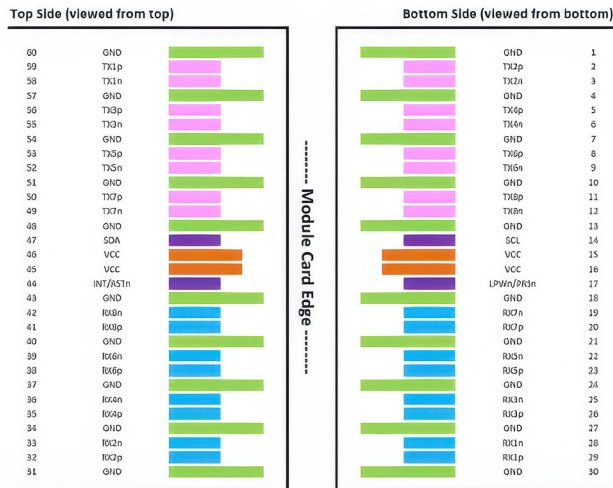
Pin	Logic	Symbol	Description
1		GND	Ground
2	CML-I	Tx2p	Transmitter Data Non-Inverted
3	CML-I	Tx2n	Transmitter Data Inverted
4		GND	Ground
5	CML-I	Tx4p	Transmitter Data Non-Inverted
6	CML-I	Tx4n	Transmitter Data Inverted
7		GND	Ground
8	CML-I	Tx6p	Transmitter Data Non-Inverted
9	CML-I	Tx6n	Transmitter Data Inverted
10		GND	Ground
11	CML-I	Tx6p	Transmitter Data Non-Inverted
12	CML-I	Tx6n	Transmitter Data Inverted
13		GND	Ground
14	LVCMOS-I/O	SCL	2-wire serial interface clock
15		VCC	+3.3V Power
16		VCC	+3.3V Power
17	Multi-Level	LPWn/PRSn	Low-Power Mode / Module Present
18		GND	Ground
19	CML-O	Rx7n	Receiver Data Inverted
20	CML-O	Rx7p	Receiver Data Non-Inverted
21		GND	Ground
22	CML-O	Rx5n	Receiver Data Inverted
23	CML-O	Rx5p	Receiver Data Non-Inverted

24		GND	Ground
25	CML-O	Rx3n	Receiver Data Inverted
26	CML-O	Rx3p	Receiver Data Non-Inverted
27		GND	Ground
28	CML-O	Rx1n	Receiver Data Inverted
29	CML-O	Rx1p	Receiver Data Non-Inverted
30		GND	Ground
31		GND	Ground
32	CML-O	Rx2p	Receiver Data Non-Inverted
33	CML-O	Rx2n	Receiver Data Inverted
34		GND	Ground
35	CML-O	Rx4p	Receiver Data Non-Inverted
36	CML-O	Rx4n	Receiver Data Inverted
37		GND	Ground
38	CML-O	Rx6p	Receiver Data Non-Inverted
39	CML-O	Rx6n	Receiver Data Inverted
40		GND	Ground
41	CML-O	Rx8p	Receiver Data Non-Inverted
42	CML-O	Rx8n	Receiver Data Inverted
43		GND	Ground
44	Multi-Level	INT/RSTn	Module Interrupt / Module Reset
45		VCC	+3.3V Power
46		VCC	+3.3V Power
47	LVCMOS—I/O	SDA	2-wire serial interface data
48		GND	Ground

49	CML-I	Tx7n	Transmitter Data Inverted
50	CML-I	Tx7p	Transmitter Data Non-Inverted
51		GND	Ground
52	CML-I	Tx5n	Transmitter Data Inverted
53	CML-I	Tx5p	Transmitter Data Non-Inverted
54		GND	Ground
55	CML-I	Tx3n	Transmitter Data Inverted
56	CML-I	Tx3p	Transmitter Data Non-Inverted
57		GND	Ground
58	CML-I	Tx1n	Transmitter Data Inverted
59	CML-I	Tx1p	Transmitter Data Non-Inverted
60		GND	Ground

OSFP Electrical Pad Layout

For detail mechanical information, please refer to the related document of OSFP MSA.



Disclaimer

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