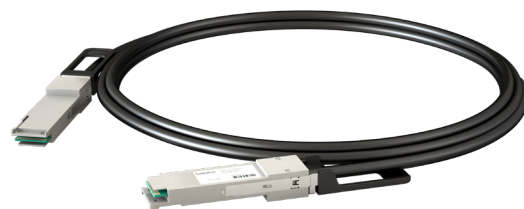


200G QSFP56 to 200G QSFP56 AEC



0.5m~10m

PVC Black

Key Features

- Support 4x53.125G PAM4
- InfiniBand HDR & Ethernet compatible
- Maximum link length up to 10m
- Single Wire Size: 28AWG~32AWG
- Maximum power consumption: 3.5W each end
- Case temperature range: 0°C to 70°C
- 3.3V Power Supply
- Hot Pluggable
- RoHS Compliance

Applications

- Data center & Networking Equipment
- Servers/Storage Devices
- High Performance Computing (HPC)
- Switches/Routers

Supported Standards

- QSFP MSA
- SFF-8636
- IEEE 802.3cd



Ordering Information

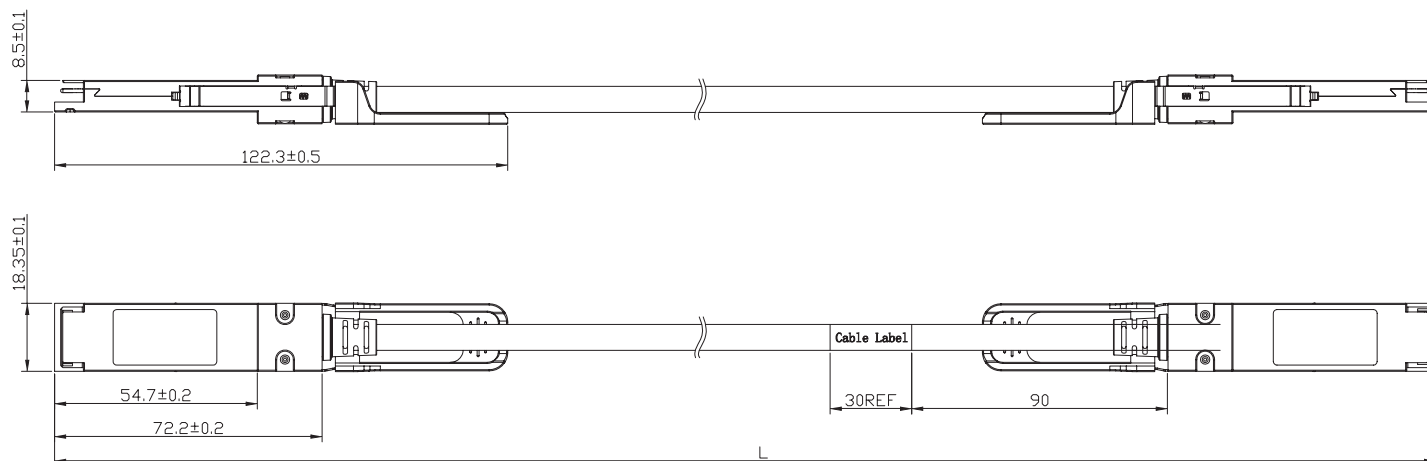
LGQQ-NK1xxxC

200G QSFP56 to 200G QSFP56 AEC – 0.5m~10m

Ordering P/Ns	Description	Length	AWG	Cable	NOTE
LGQQ-NK1060C	200G QSFP56 to 200G QSFP56 AEC	6.0m	32AWG	PVC Black	
LGQQ-NK1070C	200G QSFP56 to 200G QSFP56 AEC	7.0m	30AWG	PVC Black	
LGQQ-NK1100C	200G QSFP56 to 200G QSFP56 AEC	10.0m	28AWG	PVC Black	

xxx define AEC cable length

Mechanical Outline



Absolute Maximum Ratings

Parameter	Symbol	Unit	Min.	Typ.	Max.	Notes
Supply Voltage	Vcc	V	-0.4	-	+3.6	
Storage Temperature	TS	°C	-40	-	85	
Operating Case temperature	TOP	°C	0	-	70	
Operating Humidity	RH	%	5	-	85	

Recommended Operating Conditions

Parameter	Unit	Min.	Typ.	Max.	Notes
Operating Case Temperature	°C	0		70	
Supply Voltage	V	3.135	3.3	3.465	
Bit Rate	Gb/s		200		

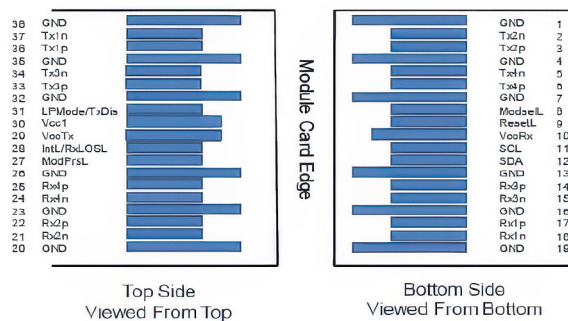
QSFP56 PIN Function Definitions

Pin	Logic	Symbol	Description
1		GND	Ground
2	CML-I	Tx2n	Transmitter Inverted Data Input
3	CML-I	Tx2p	Transmitter Non-Inverted Data Input
4		GND	Ground
5	CML-I	Tx4n	Transmitter Inverted Data Input
6	CML-I	Tx4p	Transmitter Non-Inverted Data Input
7		GND	Ground
8	LVTTL-I	ModSelL	Module Select
9	LVTTL-I	ResetL	Module Reset
10		Vcc Rx	+3.3V Power Supply Receiver
11	LVC MOS-I/O	SCL	2-wire serial interface clock
12	LVC MOS-I/O	SDA	2-wire serial interface data
13		GND	Ground
14	CML-O	Rx3p	Receiver Non-Inverted Data Output
15	CML-O	Rx3n	Receiver Inverted Data Output
16		GND	Ground
17	CML-O	Rx1p	Receiver Non-Inverted Data Output
18	CML-O	Rx1n	Receiver Inverted Data Output
19		GND	Ground
20		GND	Ground
21	CML-O	Rx2n	Receiver Inverted Data Output
22	CML-O	Rx2p	Receiver Non-Inverted Data Output
23		GND	Ground

24	CML-O	Rx4n	Receiver Inverted Data Output
25	CML-O	Rx4p	Receiver Non-Inverted Data Output
26		GND	Ground
27	LVTTL-O	ModPrsL	Present
28	LVTTL-O	IntL/RxLOSL	Interrupt/optional RxLOS
29		Vcc Tx	+3.3 V Power supply transmitter
30		Vcc1	+3.3 V Power Supply
31	LVTTL-I	LPMode/TxDis	Low Power Mode/optional TX Disable
32		GND	Ground
33	CML-I	Tx3p	Transmitter Non-Inverted Data Input
34	CML-I	Tx3n	Transmitter Inverted Data Input
35		GND	Ground
36	CML-I	Tx1p	Transmitter Non-Inverted Data Input
37	CML-I	Tx1n	Transmitter Inverted Data Input
38		GND	Ground

QSFP56 Electrical Pad Layout

For detail mechanical information, please refer to the related document of QSFP MSA.



Disclaimer

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